

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,139,037 B1  
APPLICATION NO. : 08/937877  
DATED : November 21, 2006  
INVENTOR(S) : Anatoliy V. Tsyrzanovich

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 6, line 16, the word --a-- should be added to the end of the line after the word "provide".

Claim 6, lines 16-17 should read:

--adding the adjustment signal to the input signal to provide a  
delayed output signal; and--

Column 7, line 17, a semicolon should replace the comma before the word "and" at the end of the line.

Claim 21, lines 15-17 should read:

--circuitry to constrain a phase signal within a finite preset  
range using a correction signal, wherein the correction  
signal is an integer multiple of  $2\pi$ ; and--

Signed and Sealed this

Thirtieth Day of January, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a rectangular area that has been shaded with a fine dot pattern.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*